| Title | Code |
|---|---|
| **Diploma labour** | **1010332431010330688** |

| Field | Year / Semester |
|---|---|
| **Computer science** | **2 / 3** |

| Specialty | Course |
|---|---|
| **-** | **core** |

| Hours | Number of credits |
|---|---|
| Lectures: **-**   Classes: **-**   Laboratory: **-**   Projects / seminars: **5** | **2** |
| | Language |
| | **polish** |

**Lecturer:**

Institute of Control and Information Engineering

**Faculty:**

Faculty of Electrical Engineering
ul. Piotrowo 3A
60-965 Poznań
tel. (061) 665-2539, fax. (061) 665-2548
e-mail: office_deef@put.poznan.pl

**Status of the course in the study program:**

Obligatory course of the study program for Computing Science at the Faculty of Electrical Engineering.

**Assumptions and objectives of the course:**

The main objective of the course is to present students with the principles and guidelines for the Master Thesis preparation.

**Contents of the course (course description):**

The principles of the diploma exam and guidelines for the Master Thesis preparation, regarding both editorial issues and the merits are presented. The main design assumptions of the developed projects are discussed and their implementations are presented.

**Introductory courses and the required pre-knowledge:**

Related to the thesis.

**Courses form and teaching methods:**

Projects

**Form and terms of complete the course - requirements and assessment methods:**

Assesment is based on:
- the thesis timeliness,
- project timeliness,
- project documentation,
- complete implementation of each software function.

**Basic Bibliography:**

-

**Additional Bibliography:**

-

http://www.put.poznan.pl/